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Light-modulated scanning tunneling spectroscopy for nanoscale imaging of surface photovoltage

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Light-modulated scanning tunneling spectroscopy (LM–STS) is proposed as a useful method for investigating spatially resolved surface photovoltage (SR–SPV). LM–STS provides the dependences of SR–SPV on bias voltage under constant tip-sample distance simultaneously with the entire dark/illuminated I–V curves. With this method, it is shown that SPV of a metallic Si(111) surface can be bias-dependent and SPV at zero bias voltage for Si(001) can be tip-sample-distance-dependent under conditions of small tip-sample distance and high illumination intensity. The importance of the experimental condition for interpreting experimentally obtained SR–SPV was suggested. © 2004 American Institute of Physics. [DOI: 10.1063/1.1737063]

Since the invention of scanning tunneling microscopy (STM), many studies have been devoted to the utilization of STM for measuring surface photovoltage (SPV). In general, SPV is defined as the change in surface potential of a semiconductor material caused by superband gap illumination and it provides important electric properties of the material, such as the polarity and magnitude of surface band bending, the recombination rate of photocarriers, and surface conductivity. Thus, acquiring spatially resolved SPV (SR–SPV) simultaneously with an atomic topograph by STM will lead to a greater understanding of the electric functions of nanodevices on the basis of their atomic structures.

Since the pioneering work by Hamers and Markert, several experimental methods in two categories have been developed, namely, for measuring intrinsic (zero bias) SPV and SPV under finite bias voltages, using STM. It is assumed that SPV alters the effective sample bias voltage at the tunnel junction. Hence, the lateral shift of I–V characteristics of the junction caused by the illumination is measured as SR–SPV. In the measurement of the intrinsic SPV, the disappearance of the differential tunnel conductance dI/dV at low bias voltages enhanced the small error in tunnel current detection, resulting in a large noise/artifact of the obtained intrinsic SPV. Thus, SR–SPV measurement is now often performed under finite bias voltages. In these cases, however, the local band structure beneath the STM probe can be affected by the strong electric field and high current density required for the STM observation, in addition to the effect of the contact potential difference between the probe and the sample. Hence, the result of previous studies are apparently conflicting to each other. For better understanding of the obtained SR–SPV, the measurement conditions must be carefully taken into consideration.

First, Cahill et al. reported that SPV measurement is sensitive to the tunnel current setting but not to the bias voltage; they interpreted this result using the concept of the charging effect. According to it, the tunnel current setting determines the amount of space charge under the STM probe and governs the deviation of SPV from the intrinsic value. However, McEllistrem et al. reported that SPV is strongly dependent on bias voltage when the surface has a lower density of states at the Fermi level. It was suggested that, since the low metallic surface state does not screen the electric field of the STM probe, the penetration of the electric field into the sub-surface region affects surface potential under a dark condition. On the other hand, Sommerhalter et al. insisted that both tunnel current and bias voltage affect surface potential when the surface has a negligible surface state density. In particular, they suggested that the tip-sample distance does not greatly affect the surface potential under a dark condition, but affects that under an illuminated condition.

In consideration of the importance of the SR–SPV study for the development of future electronic devices, suitable experimental conditions for SR–SPV measurement and a procedure for the analysis of the obtained results must be established. In order to clarify the disagreements among the previous results, exhaustive studies of the SPV dependence on experimental conditions will be required. In this letter, we propose a simple experimental method, light-modulated scanning tunneling spectroscopy (LM–STS), as a powerful method for such studies. LM–STS has two advantages. First, it gives the SPV dependence on experimental conditions complementary to what was obtained in previous methods. It measures the SPV dependence on bias voltage under the condition of constant tip-sample distance, in contrast to previous methods in which the condition of constant tunnel current is adopted. Second, it provides not only SR–SPV but also the complete illuminated and dark I–V curves simultaneously, providing a deeper insight into the sample properties. We apply the method to a metallic Si(111)-7×7 surface and a semiconductive Si(001)-2×1 surface, and revealed properties of SR–SPV. SR–SPV for the metallic Si(111)-7×7 surface was strongly dependent on bias voltage, particularly in the low bias voltage regime. The SPV dependence on the tip-sample distance at zero bias voltage obtained for Si(001)-2×1 revealed a fundamental difficulty of measuring the intrinsic SPV by STM potentiometry.

LM–STS was performed as follows. Mechanically chopped illumination was provided from 45° off-normal to the surface and focused onto a spot of ~0.05 mm diameter at
the tunnel junction. The problematic thermal expansion/ shrinkage of the STM tip (electrochemically etched tungsten wire, d0=25 nm) was almost negligible with a chopping frequency higher than 40 Hz and an illumination power less than 1 mW (HeCd laser, 325 and 441 nm). Note that whether the thermal effect exists or not can be judged by looking at the temporal evolution of tunnel current. \(^{12}\) STM topographs could be stably obtained under these conditions, although small modulation, \(-0.02 \text{ nm}_{\text{rms}}\) depending on the illumination and feedback conditions, could be observed due to the intermittent SPV effect. In order to obtain an SPV spectrum, the scanning was stopped and a conventional STS measurement was performed under the chopped illumination. Part of a typical result for an \(n\)-type Si(111)-7 \(\times\) 7 sample (30 \(\Omega\) cm) is shown in Fig. 1(a). The \(I\text{--}V\) curve (red) oscillates widely between the two virtual \(I\text{--}V\) curves (dotted black), corresponding to the dark and illuminated conditions. The SPV spectrum is acquired by plotting the lateral separation between these two virtual curves against the bias voltage under the dark condition, as shown in Figs. 1(b) and 1(c).

In this work, \(I\text{--}V\) curve consisting of two thousand points was obtained in 1–2 s with a chopping frequency of \(\sim 40\) Hz. The tunnel current for 30%–80% of each illuminated/dark period was averaged to extract the illuminated/dark current, which are shown by the blue and green circles, respectively, in Fig. 1(b). The gate time was introduced to eliminate artifacts due to the finite transient time of illumination during which the laser power changes from 0% to 100% and vice versa, from the displacement current as a result of capacitance change of the tunnel junction, and from the recovery time of the metal–insulator–semiconductor (MIS) junction at the tunnel gap.\(^{13}\) Finally, the illuminated/dark \(I\text{--}V\) curves for calculating SPV, which are shown by the dotted black lines in Fig. 1(a), are obtained by interpolating the measured points using the smoothing spline function. In our case, the precision of the \(I\text{--}V\) curves was limited by a few percent of relative error due to mechanical vibration and a few picoamperes of absolute error due to broadband noise in the current signal. These errors in the tunnel current, together with the magnitude of \(dI/dV\), determine the detection limit of SPV. When the tip-sample distance is longer, \(dI/dV\) in the low-bias-voltage regime becomes too small. Accordingly, SPV could be measured only for the high-bias-voltage regime in such cases.

The left-hand column of Fig. 2 shows SPV spectra of a \(p\)-type Si(001)-2 \(\times\) 1 surface (0.03 \(\Omega\) cm) with various tip-sample distances. The distances were determined by the servo action of conventional STM electronics under chopped illumination with changing the bias voltage from (a) 0.3 to (d) 0.6 V with a fixed reference current of 1.0 nA. For each tip-sample distance, 25 spectra were measured at random points on the surface. The dotted lines represent individual SPV spectra and the solid black ones represent the averaged ones. It can be seen that, even under a same servo condition, the SR–SPV spectra scatter widely. This is not because of the spatial variety of SPV but mainly because of the uncertainty in determining the tip-sample distance. The uncertainty arises from the fact that the tip-sample distance fluctuates when the servo action is active because it tries to compensate the intermittent SPV effect. Since each STS measurement is done asynchronously to the illumination, the tip-sample distance differs from spectrum to another. To visualize the amount of fluctuation, the dotted spectra are colored by the tunnel current at a bias voltage of \(-0.5\) V. The color scale of red to violet corresponds to 70%–130% of the tunnel current compared with the averaged value. In the right-hand column of Fig. 2, the SR–SPV at zero bias voltage (intrinsic SR–SPV) is summarized in histograms.

In the remainder of this letter, we discuss the above results.
results. Figure 1 clearly shows that SR–SPV can be strongly dependent on bias voltage even for a surface with a metallic surface state, such as the Si(111)-7×7 surface. Namely, the SPV value increased linearly in the low-voltage regime ($|V_s|<0.1$ V) but showed saturation in the high-voltage regimes ($V_s<-0.1$ V and $0.1$ V<$V_s$). The curves in these three regimes, with different slopes, were also confirmed in SR–SPV spectra for a nonmetallic Si(001)-2×1 surface, as shown in Fig. 2. These spectra cannot be simply explained in terms of the charging effect because the tunnel current changes more in the high-bias-voltage regimes than in the low-bias-voltage regime. Instead, penetration of the electric field due to the bias voltage into the subsurface region under dark condition and the amount of accumulated carriers under illuminated condition should be considered. Sommerhalter et al. reported that the surface band bending of the nonmetallic p-type WS$_2$ surface under STM measurement can be qualitatively represented by a simple one-dimensional MIS junction model. The three regimes observed in the SPV spectra for p-type Si(001) surface can be explained in accordance with their result as follows. In the high-positive-voltage regime ($0.1$ V<$V_s$), sufficient accumulation of majority carrier occurs under dark condition. Hence, the little band bending under the dark condition results in a small SPV. At a negative bias voltage, depletion of majority carrier occurs and the amount of surface band bending is proportional to the bias voltage under the dark condition. In the low-bias-voltage regime ($|V_s|<0.1$ V), the band bending is fully relaxed by illumination, resulting in the linear dependence of SPV on bias voltage. At high negative bias voltages ($V_s<-0.1$ V), the amount of band bending relaxation saturates because of the limited backdiffusion of minority carrier.

In addition to bias voltage dependence, the SPV dependence on the tip-sample distance can also be explained by the same model. In Fig. 2, a small tip-sample distance correspondings to a small SPV. In addition, the same tendency was obtained in the comparison of the dotted spectra shown in the same plot; the one with a larger current showed a smaller SPV. When the tip-sample distance is small, the photogenerated carriers tunnel efficiently into the STM tip and the accumulation of carriers at the surface region is insufficient, preventing the complete relaxation of band bending. Furthermore, the SR–SPV at zero bias voltage was also dependent on the tip-sample distance, as shown in the right-hand column of Fig. 2. This is rather surprising, since there is no tunnel current flowing between the tip and the sample in this situation. We conjecture that the very high tunnel transmission coefficient between the tip and the sample pins the surface potential, preventing the band bending relaxation. In some of the previous studies on the SPV at zero bias voltage, shortening of the tip-sample distance was attempted in order to increase the measurement accuracy. Our result revealed the fundamental limitation of such approach to measure the precise SPV at zero bias voltage.

Before summarizing the argument, we mention the high spatial resolution of LM–STS method. Figure 3 shows an SPV mapping in a color scale overlapped on a STM topograph. Silver was deposited from a tungsten filament on a clean n-type Si(001) substrate (0.005 $\Omega$ cm) at room temperature and annealed at 400 °C. About 40% of the surface was covered by the two-dimensional silver islands of the 2×3 structure (indicated by the green lines in Fig. 3) in accordance with literature. During the STM imaging over the 30×30 nm area ($I_t=1.0$ nA and $V_s=-2.0$ V) at 80 K, 27×27 LM–STS spectra were taken at the grid points (white dots in Fig. 3). From each spectrum, SR–SPV value for $V_s=2.4$ V was extracted and shown in the color scale. SPV values between the grid points were calculated by linear interpolation. Nanometer scale spatial variation of SPV due to the existence of silver islands was successfully visualized simultaneously with obtaining an atomically resolved STM topograph. The detail will be reported elsewhere.

In summary, LM–STS was proposed as an easily accessible and very powerful method for investigating SR–SPV under various experimental conditions. Unlike previous methods, it can obtain not only the SR–SPV spectrum but also the entire illuminated/dark $I$–$V$ characteristics with a constant tip-sample distance. With this method, it was shown that the experimentally obtained SR–SPV is dependent on the bias voltage for a metallic Si(111) surface, and on the tip-sample distance for a semiconductive Si(001) surface even at zero bias voltage.